

FIG. 1

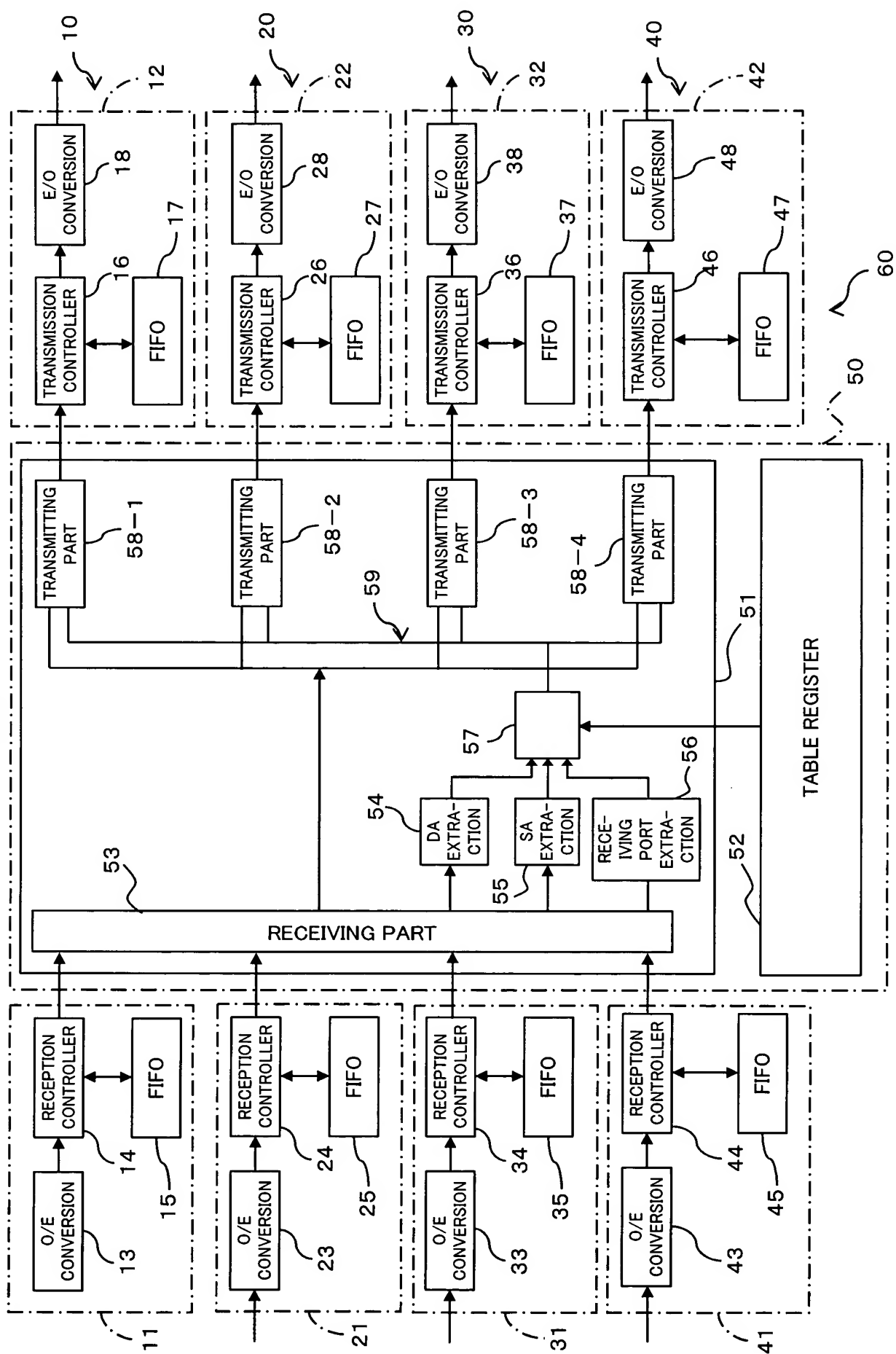


FIG. 2

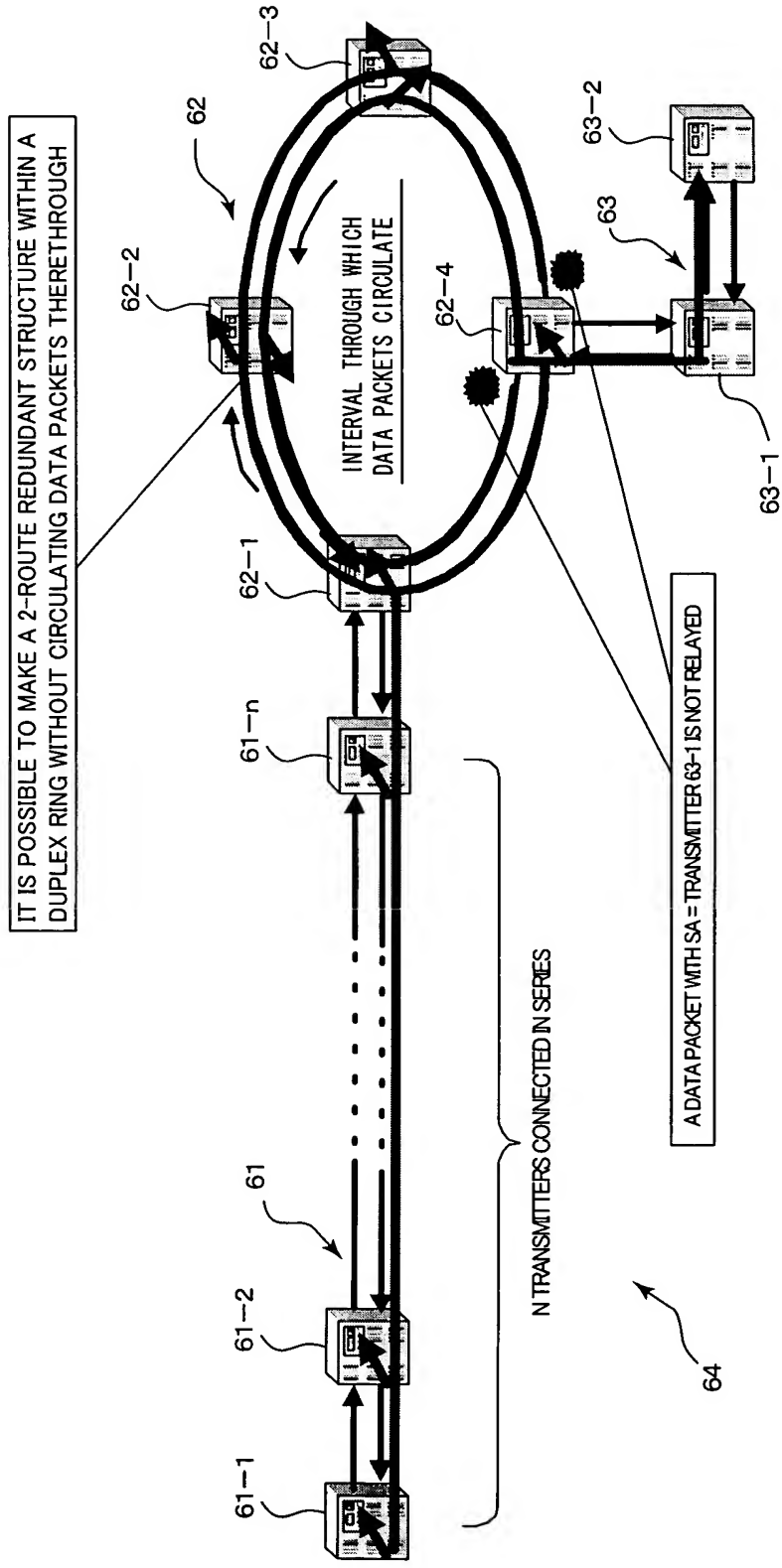


FIG. 3

RECEIVING PORT	RELAY	COMPARISON WITH EXTRACTED SA
TRANSMISSION PATH PORT 10	O	TRANSMITTERS 61-1~61-n
	x	TRANSMITTERS 62-1~62-4,63-1,63-2
TRANSMISSION PATH PORT 20	O	TRANSMITTERS 62-2~62-4,63-1,63-2
	x	TRANSMITTERS 61-1~61-n,62-1
TRANSMISSION PATH PORT 30	O	TRANSMITTERS 62-2~62-4,63-1,63-2
	x	TRANSMITTERS 61-1~61-n,62-1

FIG. 4

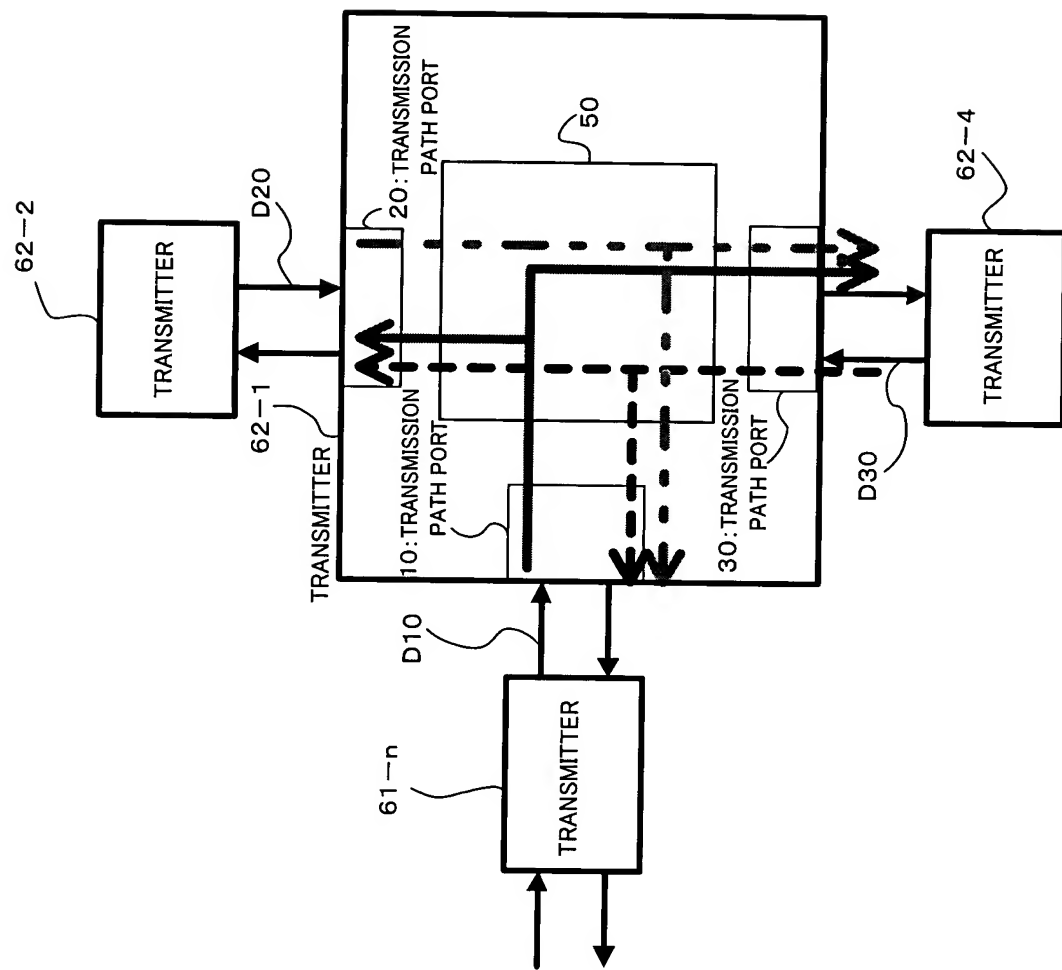


FIG. 5

RECEIVING PORT	RELAY	COMPARISON WITH EXTRACTED SA
TRANSMISSION PATH PORT 10	O	TRANSMITTERS 61-1 ~ 61-n, 62-1 ~ 62-3
	x	TRANSMITTERS 62-4, 63-1, 63-2
TRANSMISSION PATH PORT 20	O	TRANSMITTERS 61-1 ~ 61-n, 62-1 ~ 62-3
	x	TRANSMITTERS 62-4, 63-1, 63-2
TRANSMISSION PATH PORT 30	O	TRANSMITTERS 63-1, 63-2
	x	TRANSMITTERS 61-1 ~ 61-n, 62-1 ~ 62-4

FIG. 6

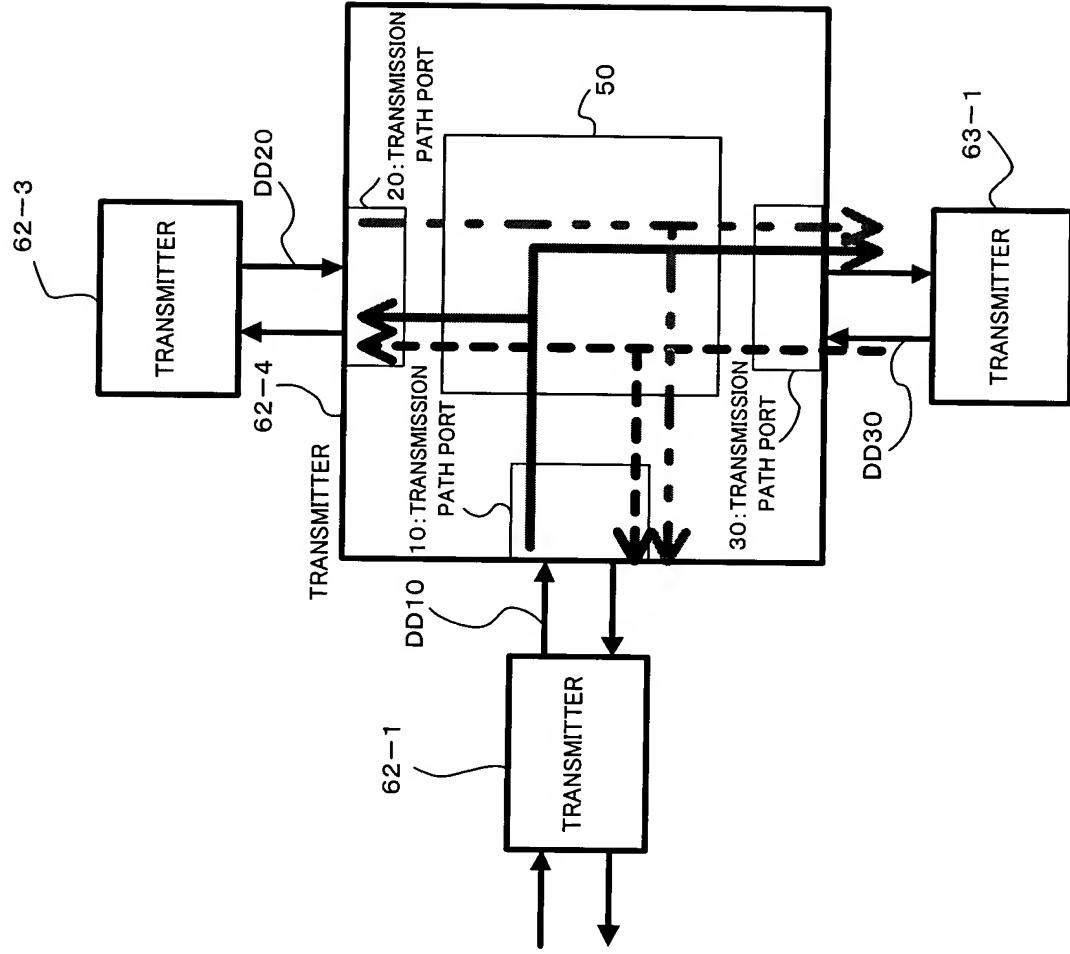


FIG. 7

RECEIVING PORT	RELAY	COMPARISON WITH EXTRACTED SA
TRANSMISSION PATH PORT 10	O	OTHER THAN A SELF-TRANSMITTER
	x	SELF-TRANSMITTER
TRANSMISSION PATH PORT 20	O	OTHER THAN A SELF-TRANSMITTER
	x	SELF-TRANSMITTER
TRANSMISSION PATH PORT 30	O	OTHER THAN A SELF-TRANSMITTER
	x	SELF-TRANSMITTER

FIG. 8

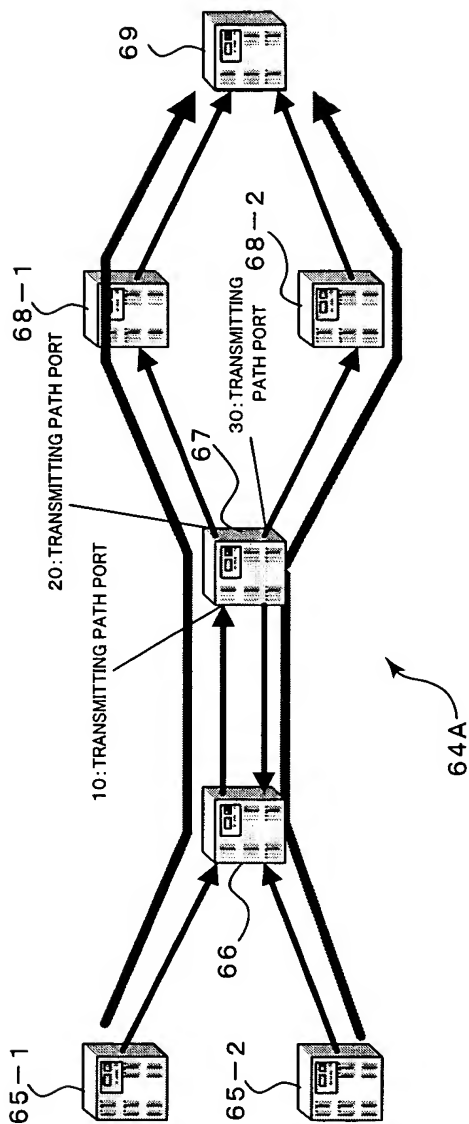


FIG. 9

ROUTING INFORMATION FOR TRANSMITTER 67

RECEIVING PORT	TRANSMITTING PORT	RELAY	COMPARISON WITH EXTRACTED DA	COMPARISON WITH EXTRACTED SA
TRANSMISSION PATH PORT 10	TRANSMISSION PATH PORT 20	O	TRANSMITTER 69	TRANSMITTER 65-1
		x	TRANSMITTER 69	TRANSMITTER 65-2
TRANSMISSION PATH PORT 10	TRANSMISSION PATH PORT 30	O	TRANSMITTER 69	TRANSMITTER 65-2
		x	TRANSMITTER 69	TRANSMITTER 65-1

FIG. 10(a)

RECEIVING PORT	TRANSMITTING PORT	RELAY	COMPARISON WITH EXTRACTED DA	COMPARISON WITH EXTRACTED SA	
TRANSMISSION PATH PORT 10	TRANSMISSION PATH PORT 20	O	TRANSMITTER m1	TRANSMITTER s1	SETTINGS TAKING DA/SA/TRANSMITTING PORT/ RECEIVING PORT INTO CONSIDERATION
TRANSMISSION PATH PORT 10	TRANSMISSION PATH PORT 20	x	TRANSMITTER m2	TRANSMITTER s1	

FIG. 10(b)

RECEIVING PORT	送信ポート	RELAY	COMPARISON WITH EXTRACTED DA	抽出したSAと比較	
TRANSMISSION PATH PORT 10	DON'T CARE	O	TRANSMITTER m1	TRANSMITTER s1	TAKE DA/SA/TRANSMITTING PORT/RECEIVING PORT INTO CONSIDERATION AND TRANSMIT DATA TO ALL PORTS
TRANSMISSION PATH PORT 10	DON'T CARE	x	TRANSMITTER m2	TRANSMITTER s1	

FIG. 11

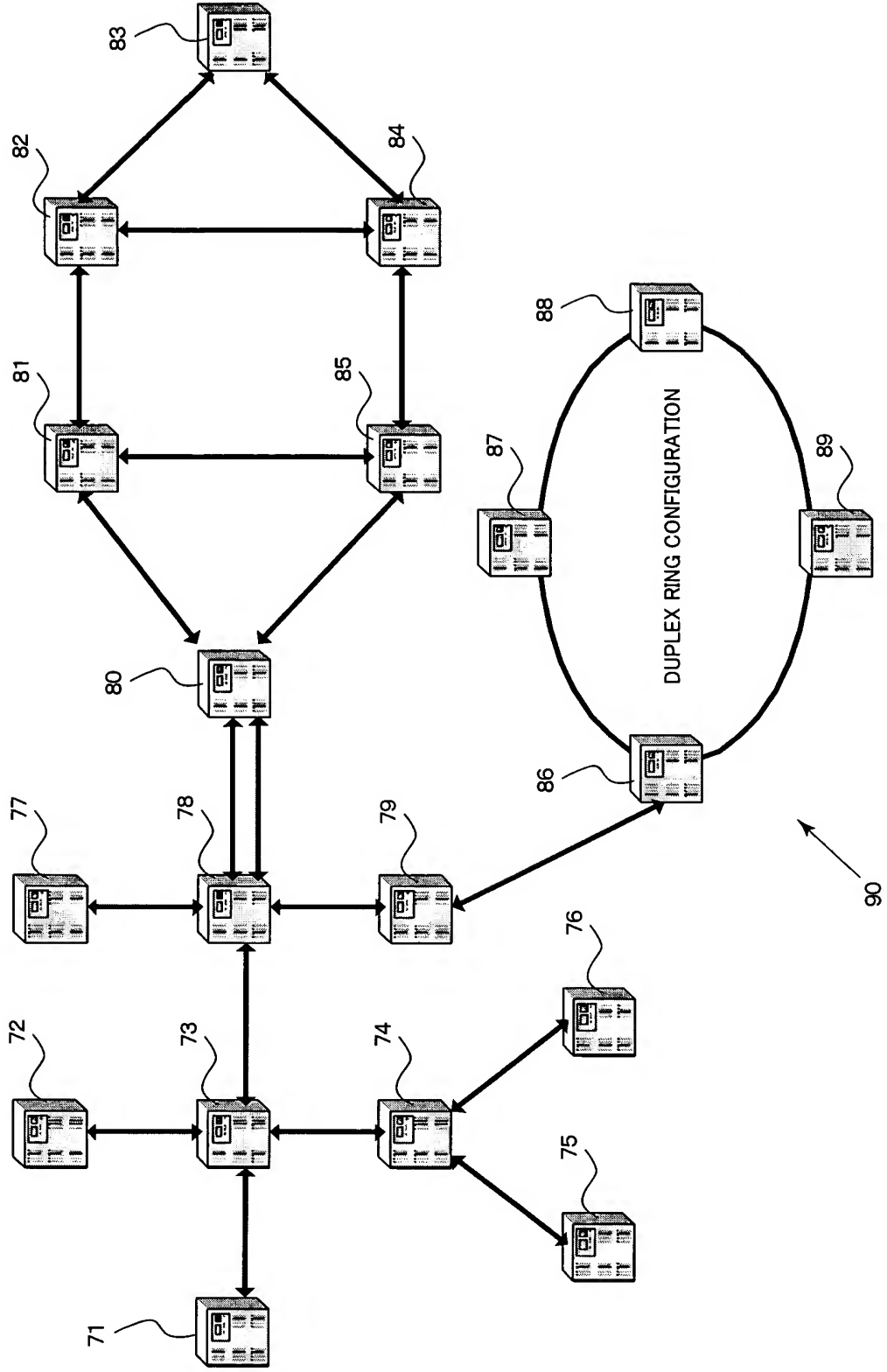
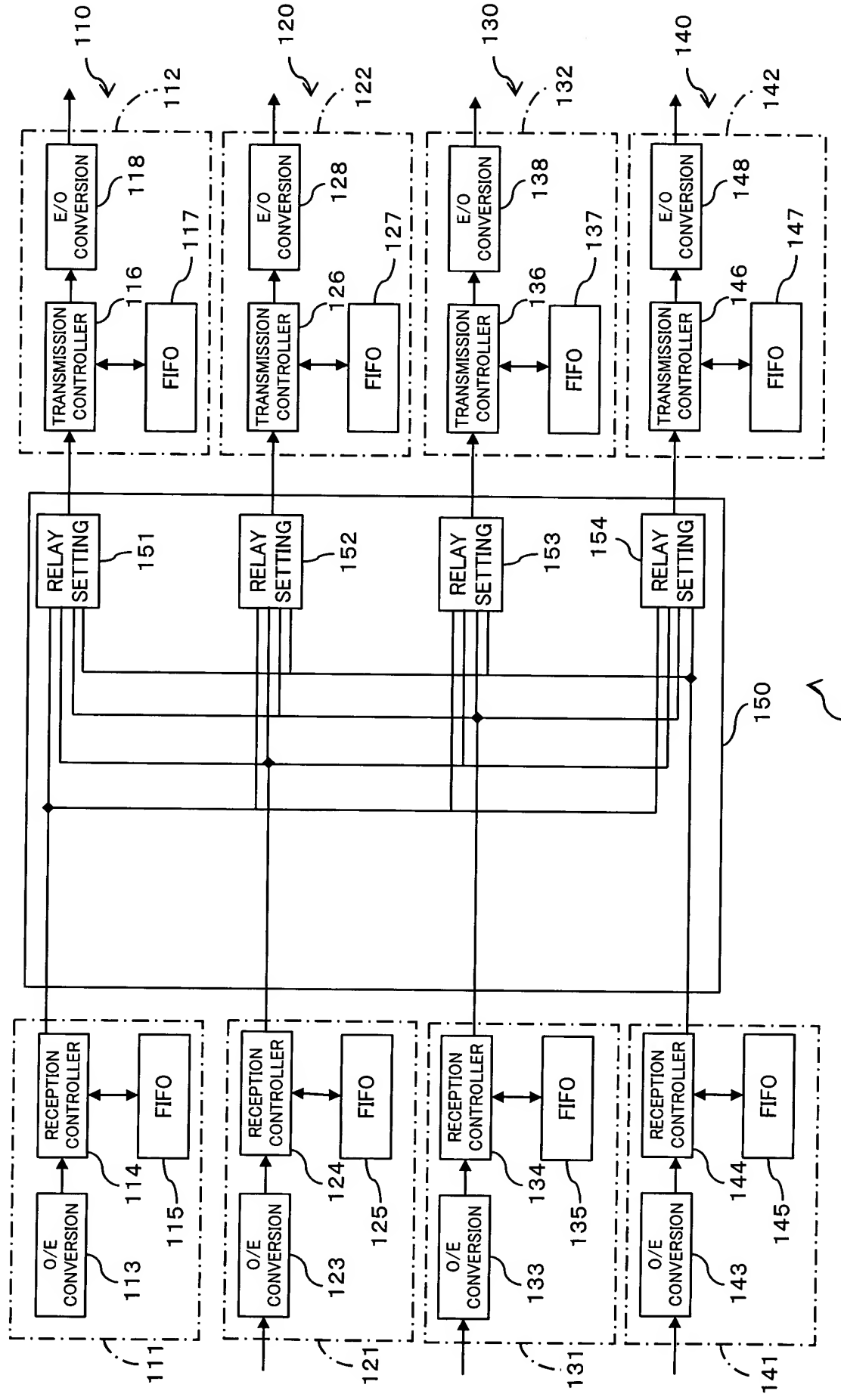


FIG. 12



1-1~1-n, 2-1~2-4, 3-1, 3-2

FIG. 13

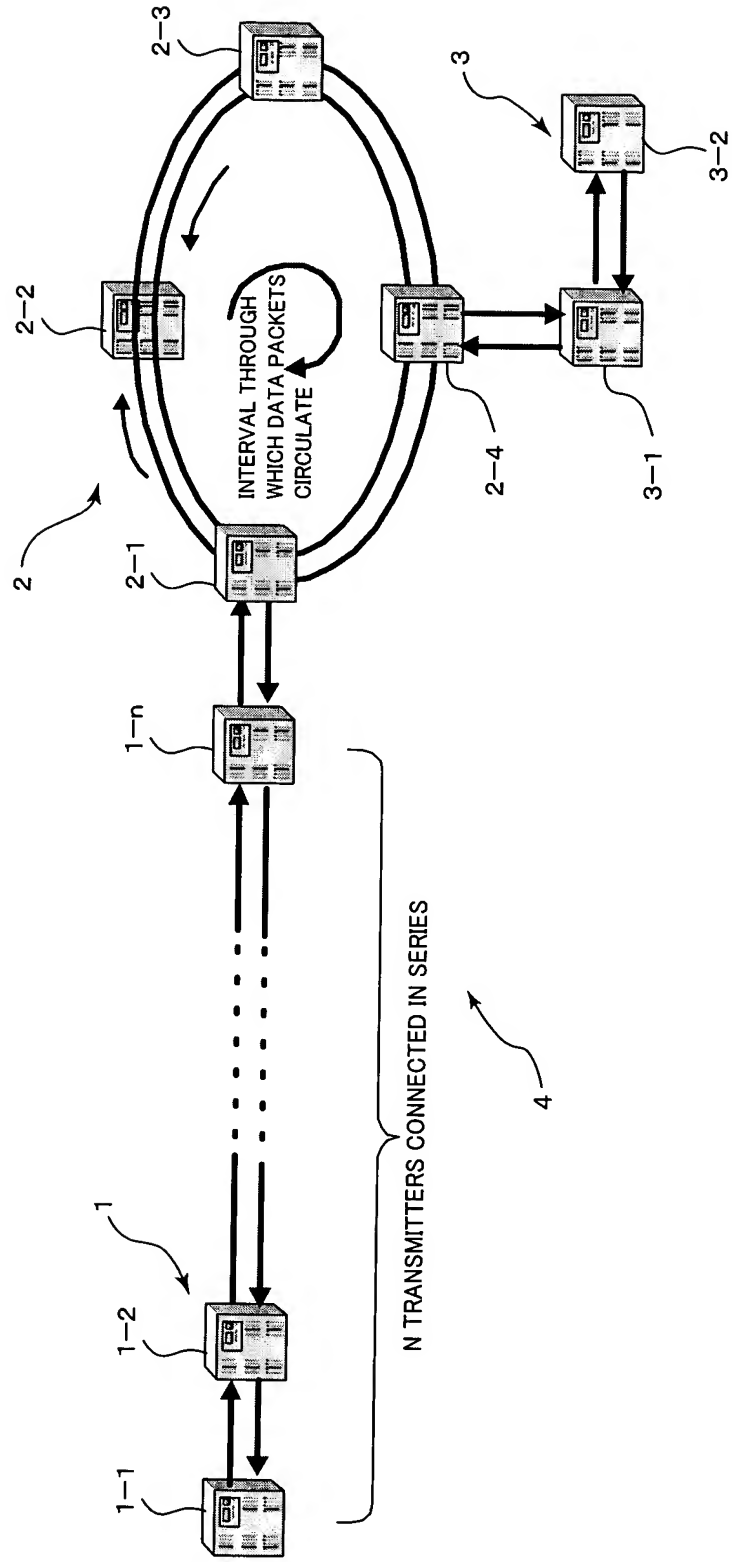


FIG. 14

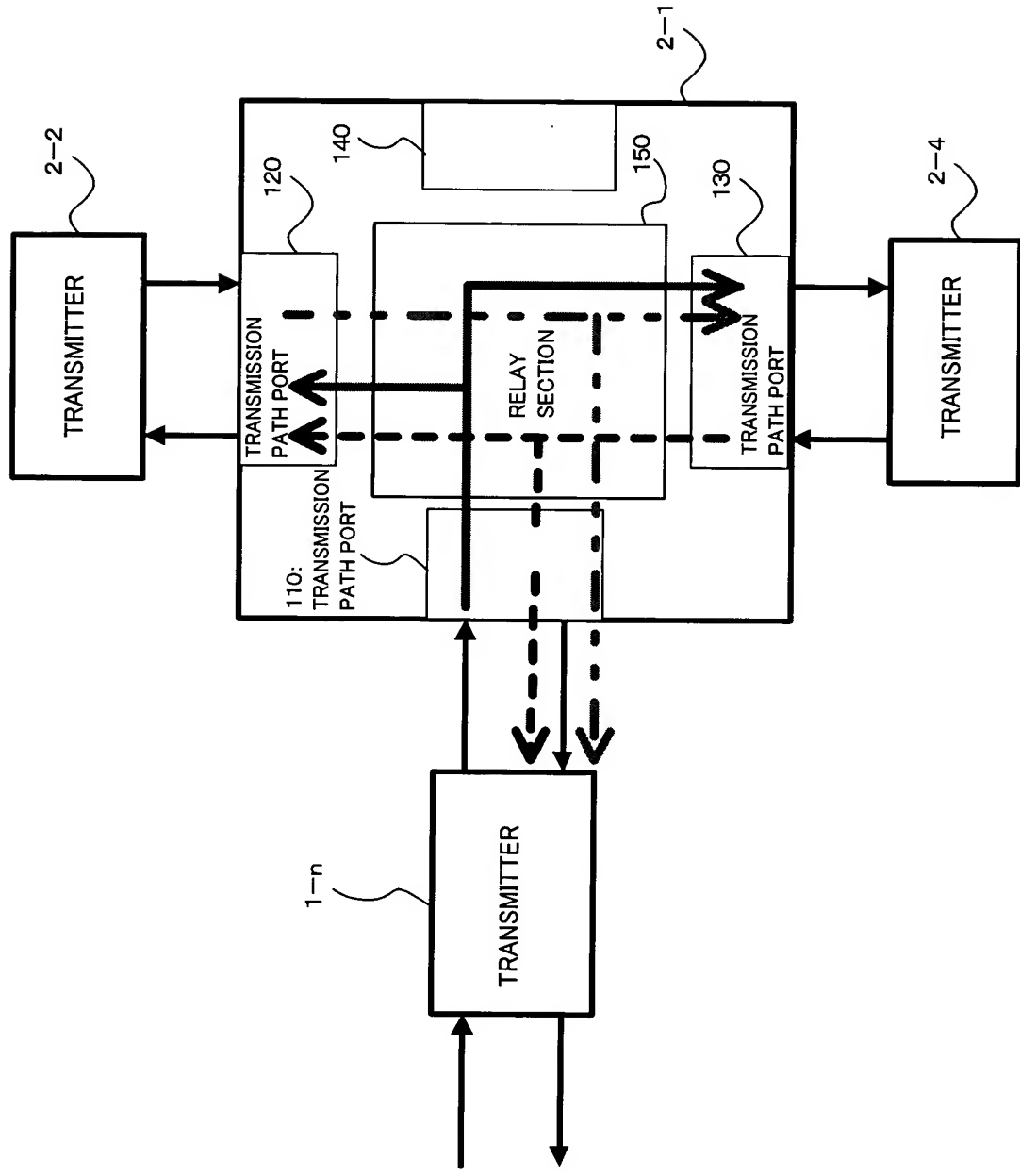


FIG. 15

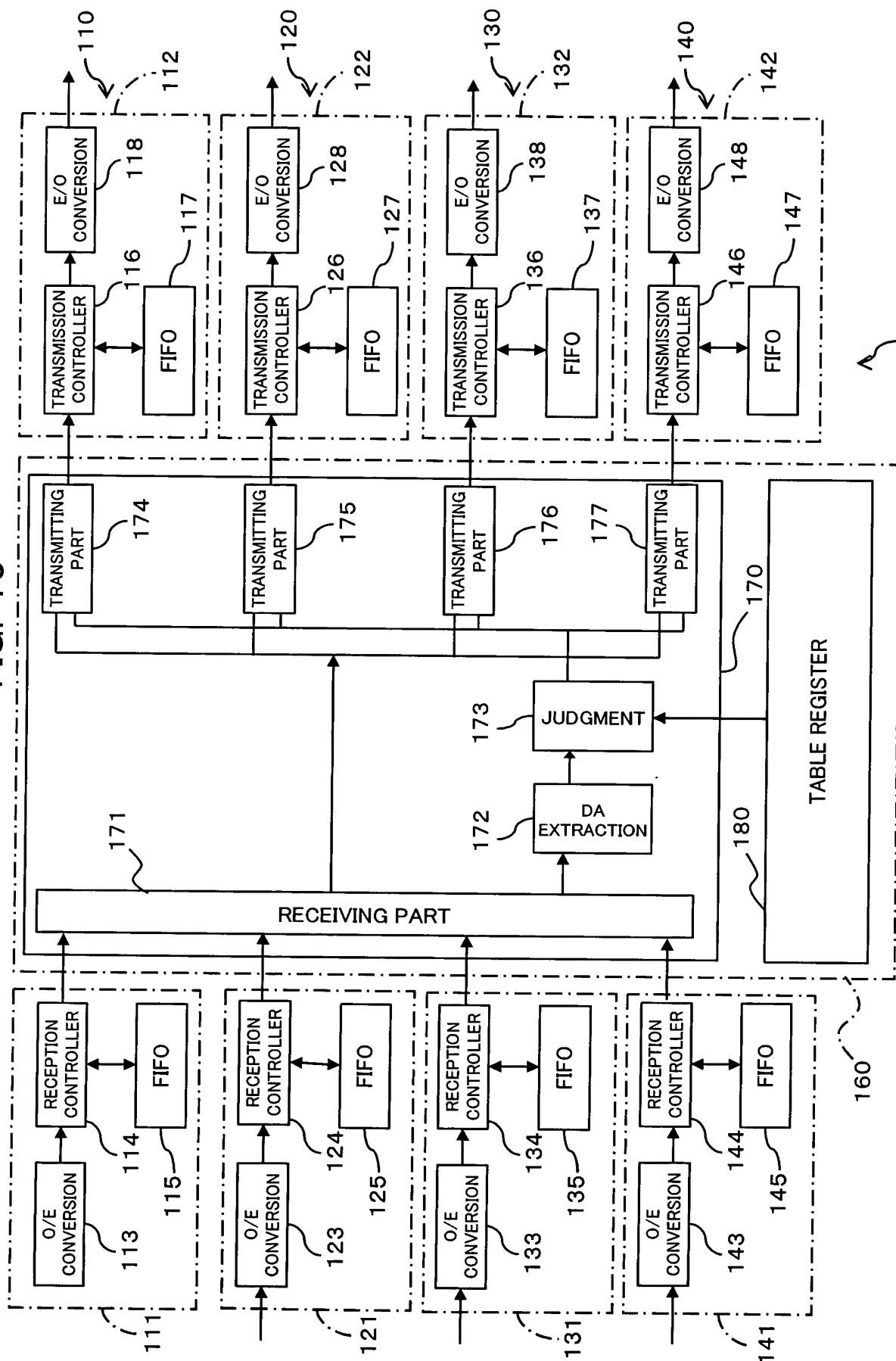


FIG. 16

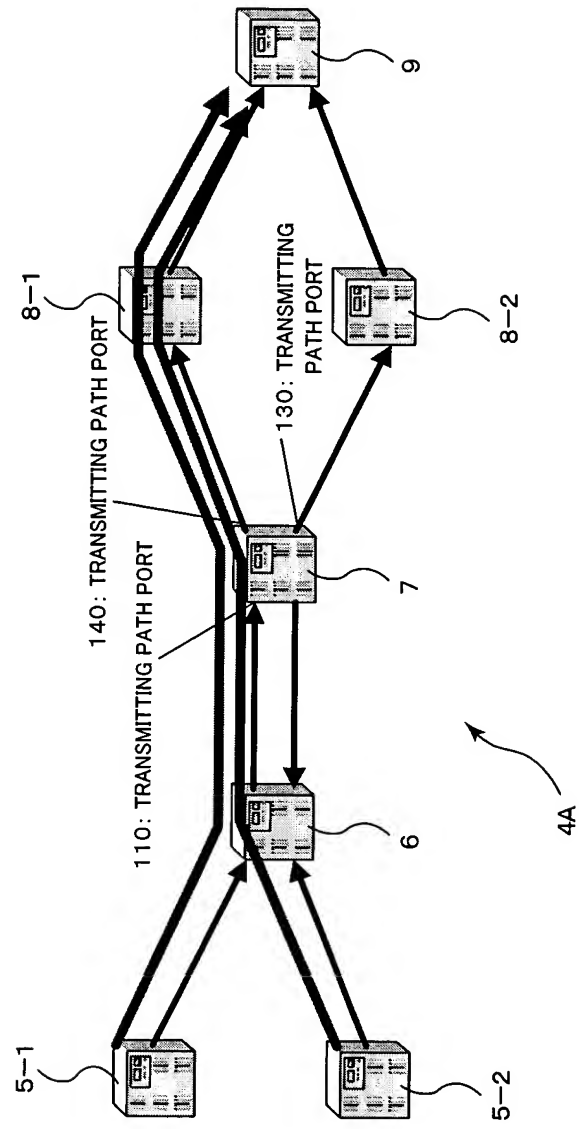


FIG. 17

